

US-PAT-NO: 6173343

DOCUMENT-IDENTIFIER: US 6173343 B1

TITLE: Data processing system and method with central  
processing unit-determined peripheral device service

----- KWIC -----

Application Filing Date - AD (1):  
19980918

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19970919

Brief Summary Text - BSTX (5):

In consequence, the patent proposes dynamically switching from an interrupt  
driven mode to a polling mode in order to reduce latency.

Brief Summary Text - BSTX (10):

This invention provides a data processing system of the above described type  
characterised in that the rate of polling in the timed mode is less than the  
threshold frequency. In this way, the load on the processor caused by frequent  
interrupts is reduced.